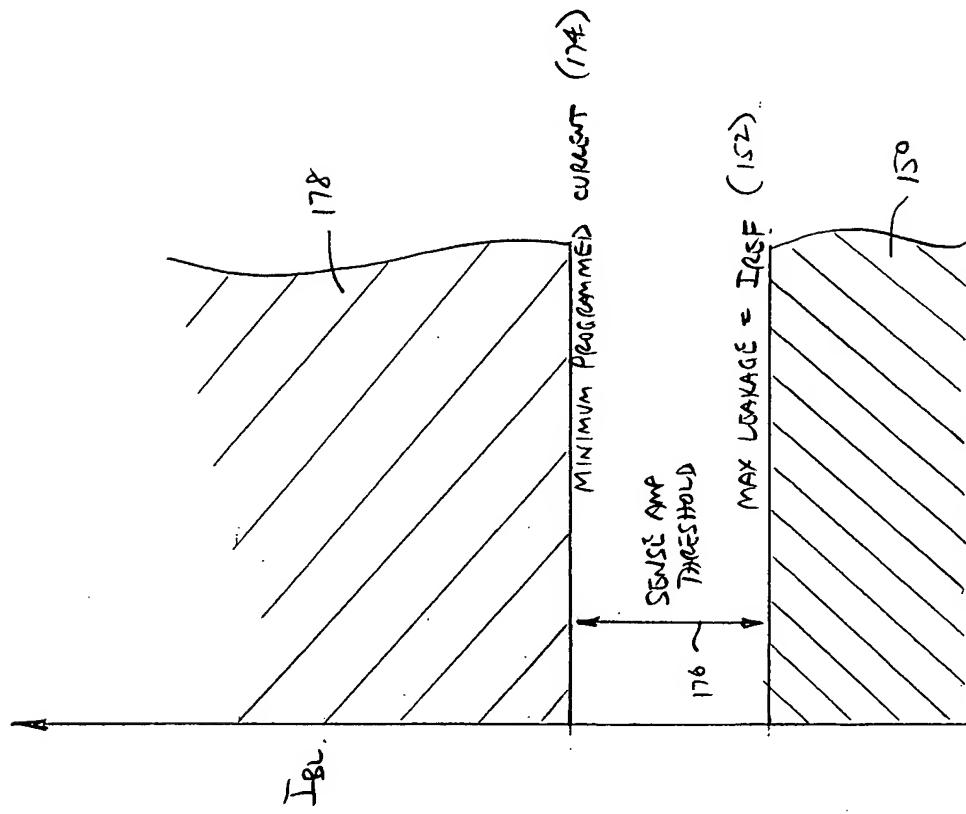
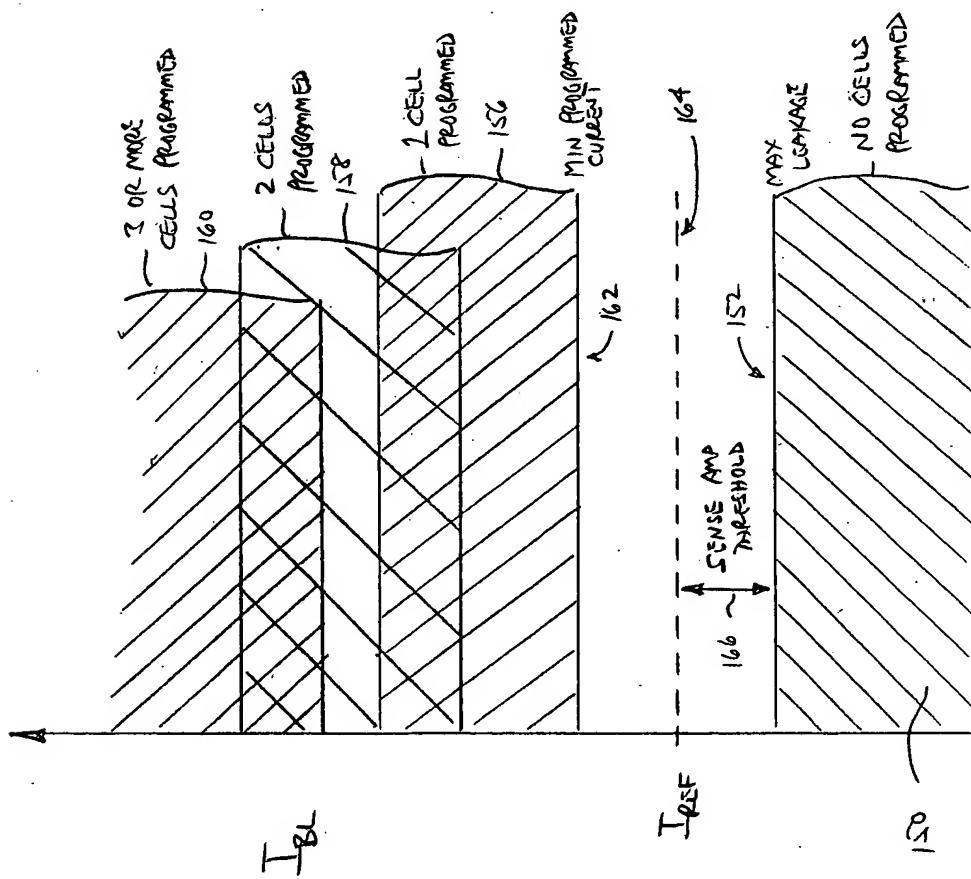


Fig. 1



TWIN CELL

Fig. 3



BINARY DECISION  
WITH REFERENCE

Fig. 2

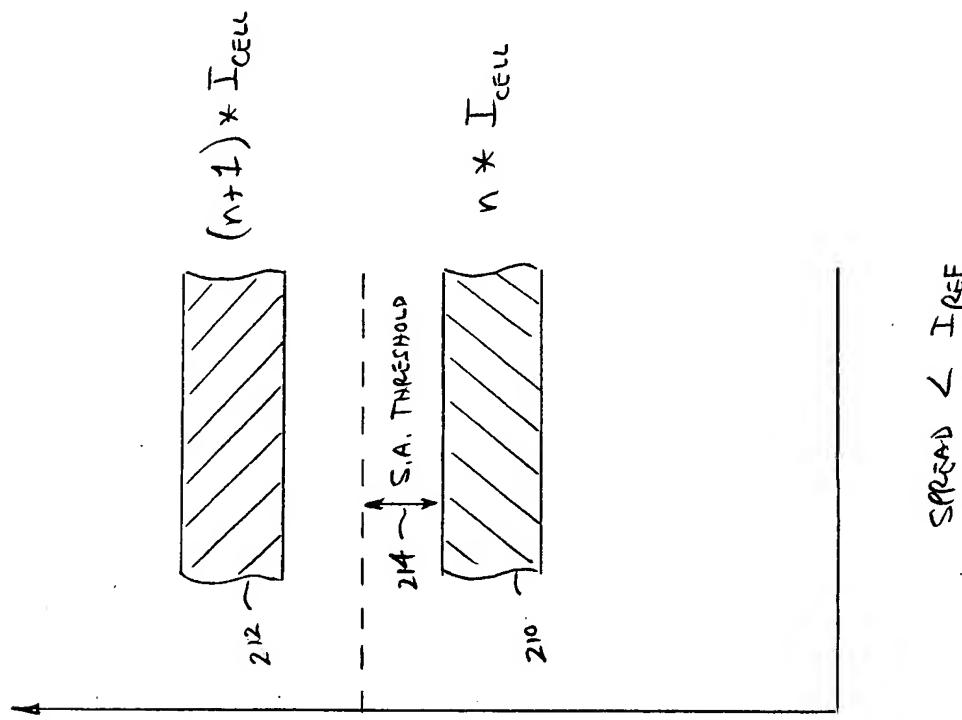


Fig. 5

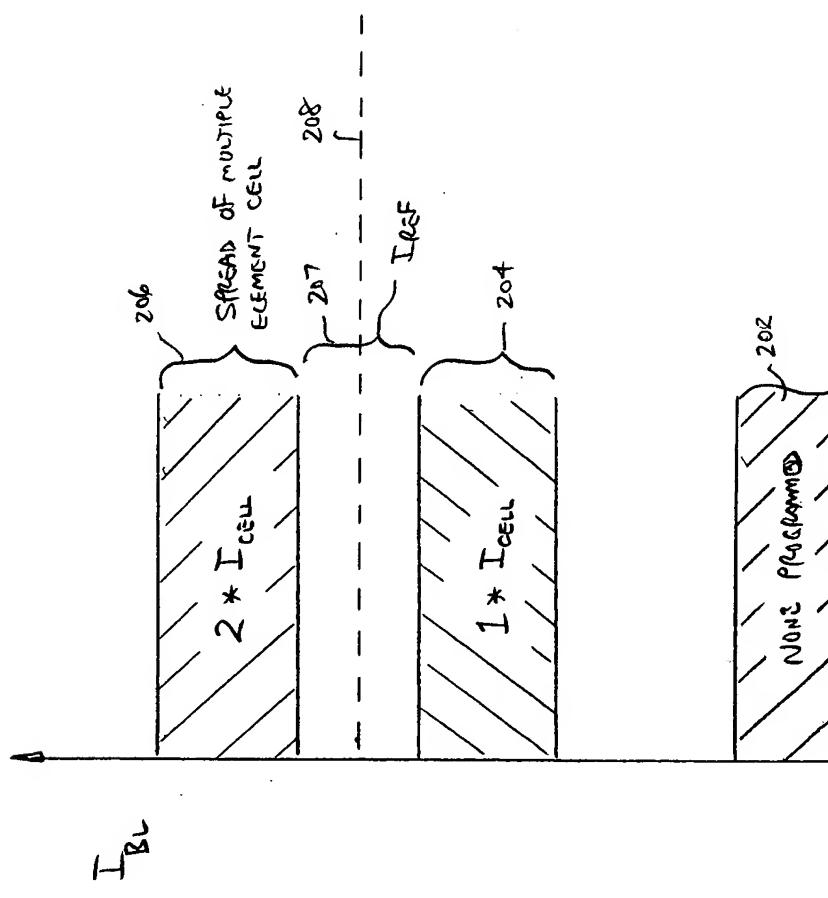


Fig. 4

**Multiple Twin Cell Non-Volatile Memory Array and Logic Block Structure and Method Therefor**  
**Roy E. Scheuerlein, et al.**  
**Attorney Docket No.: 023-0024**

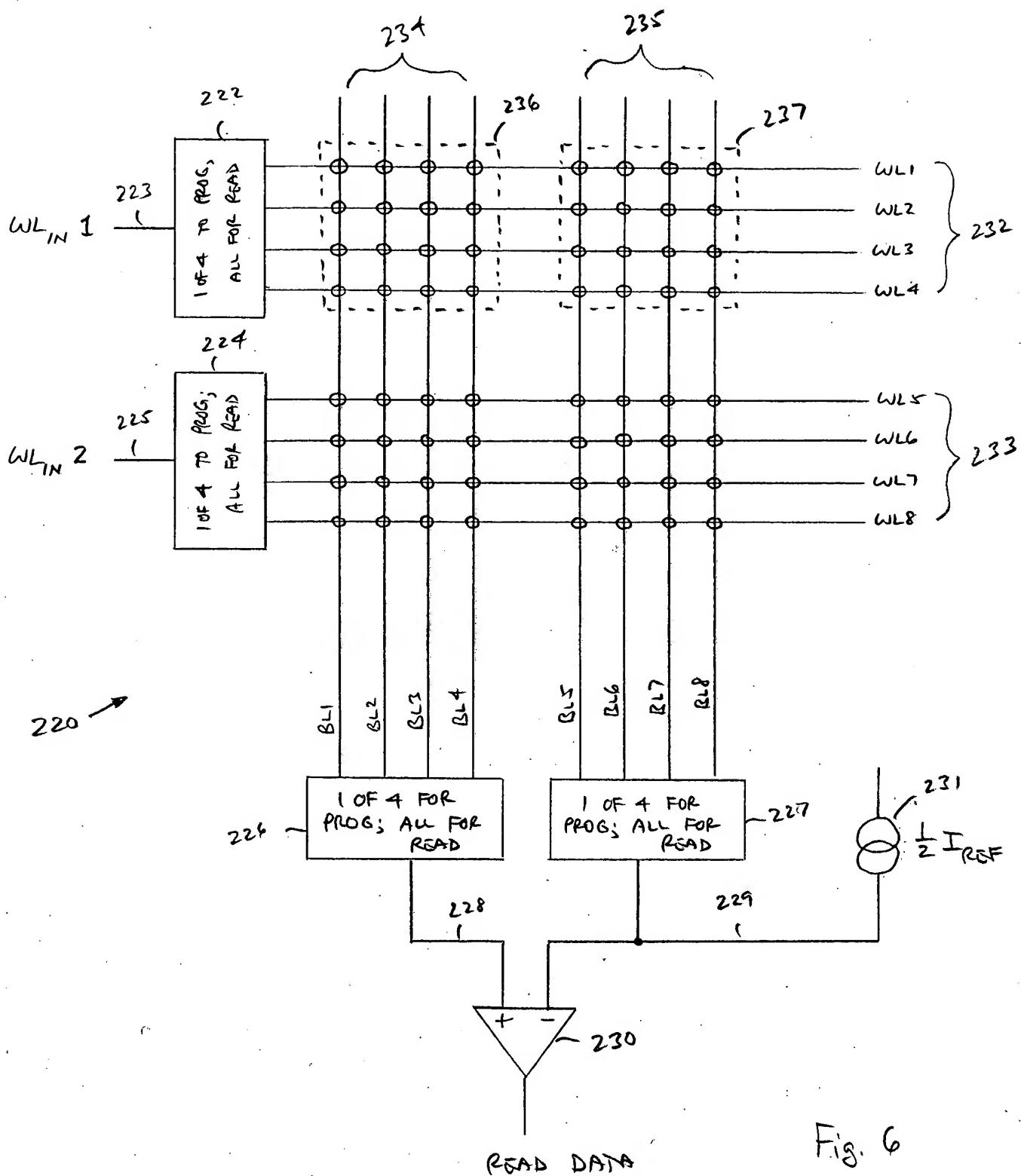
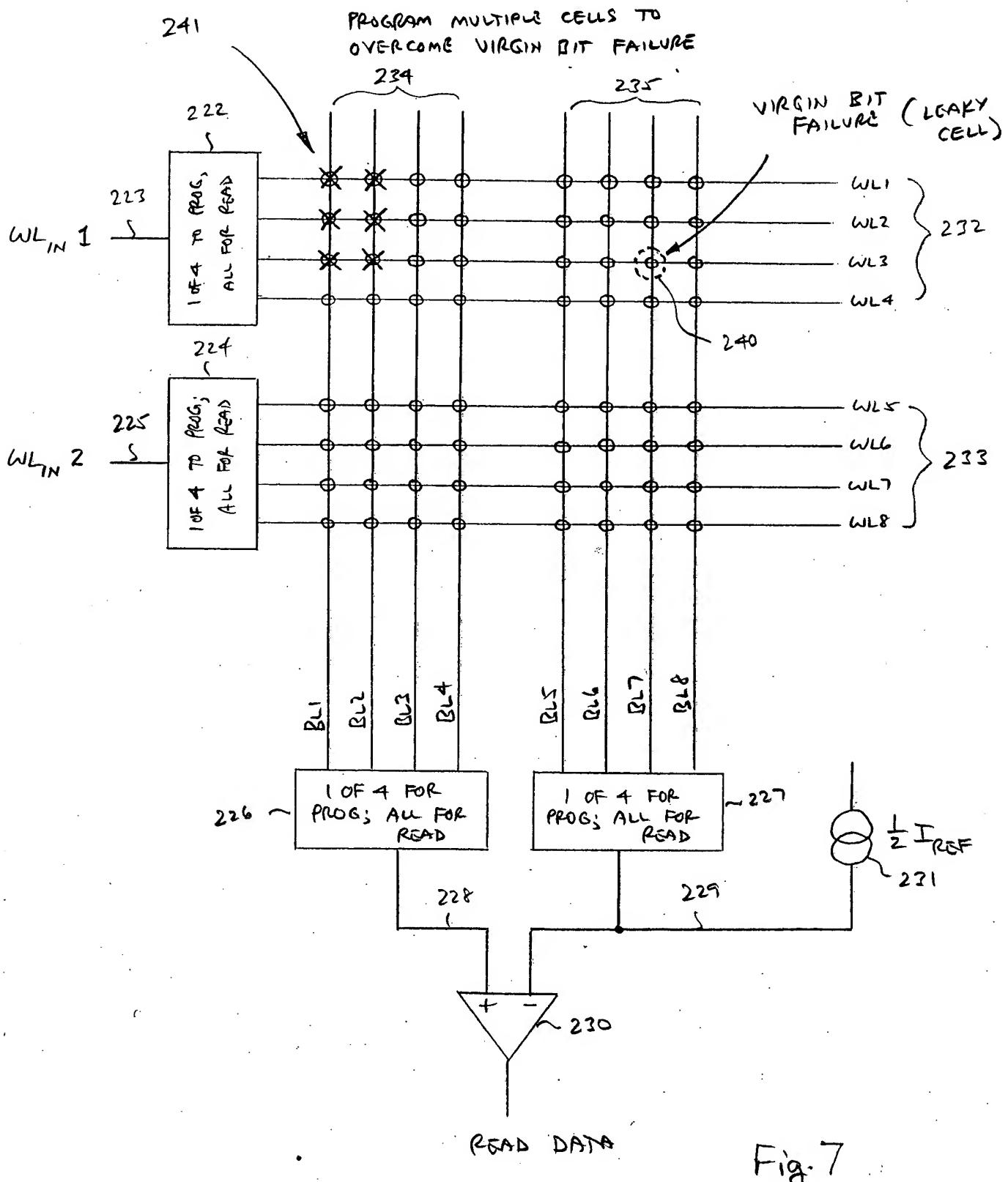


Fig. 6



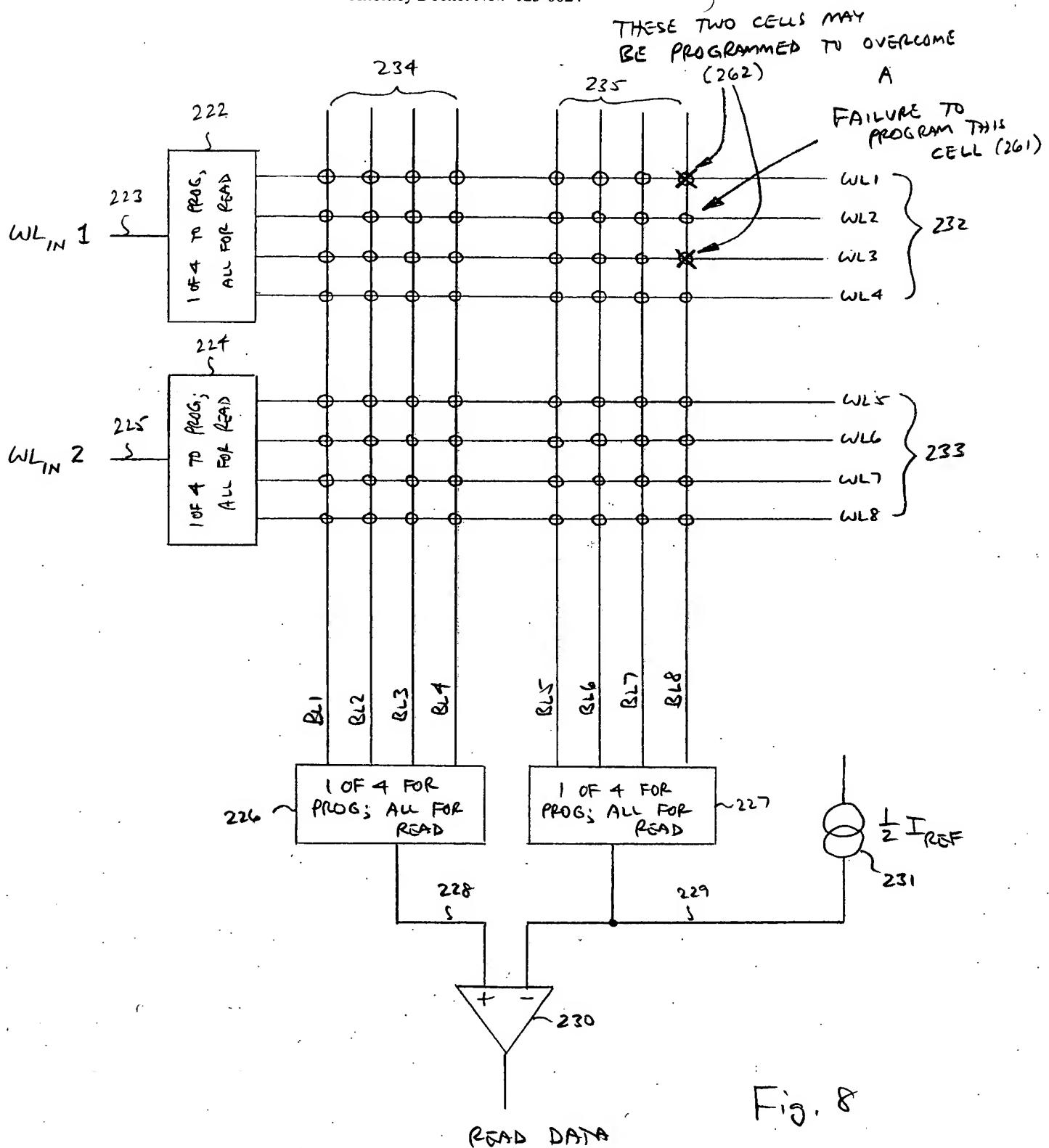


Fig. 8

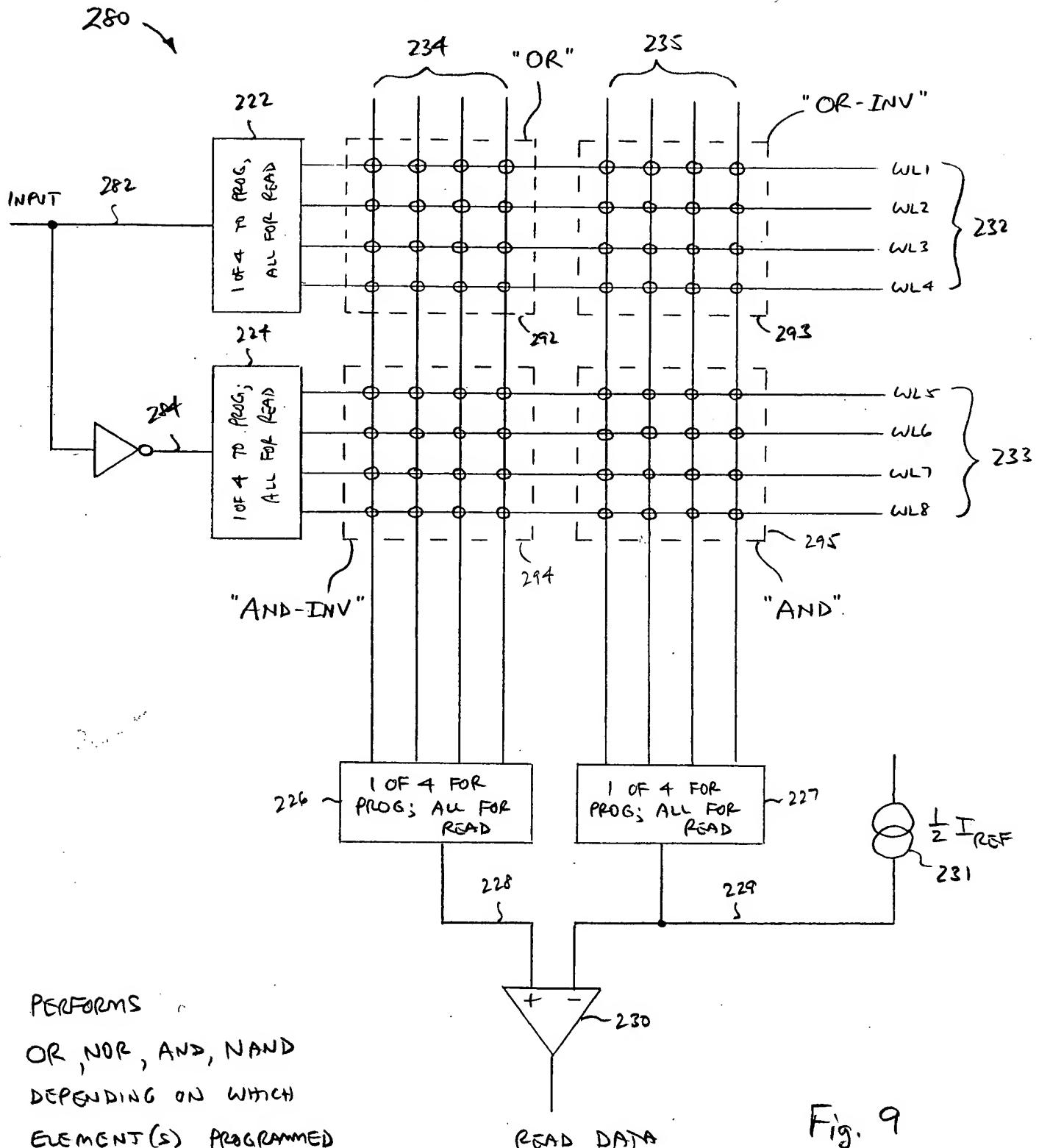


Fig. 9

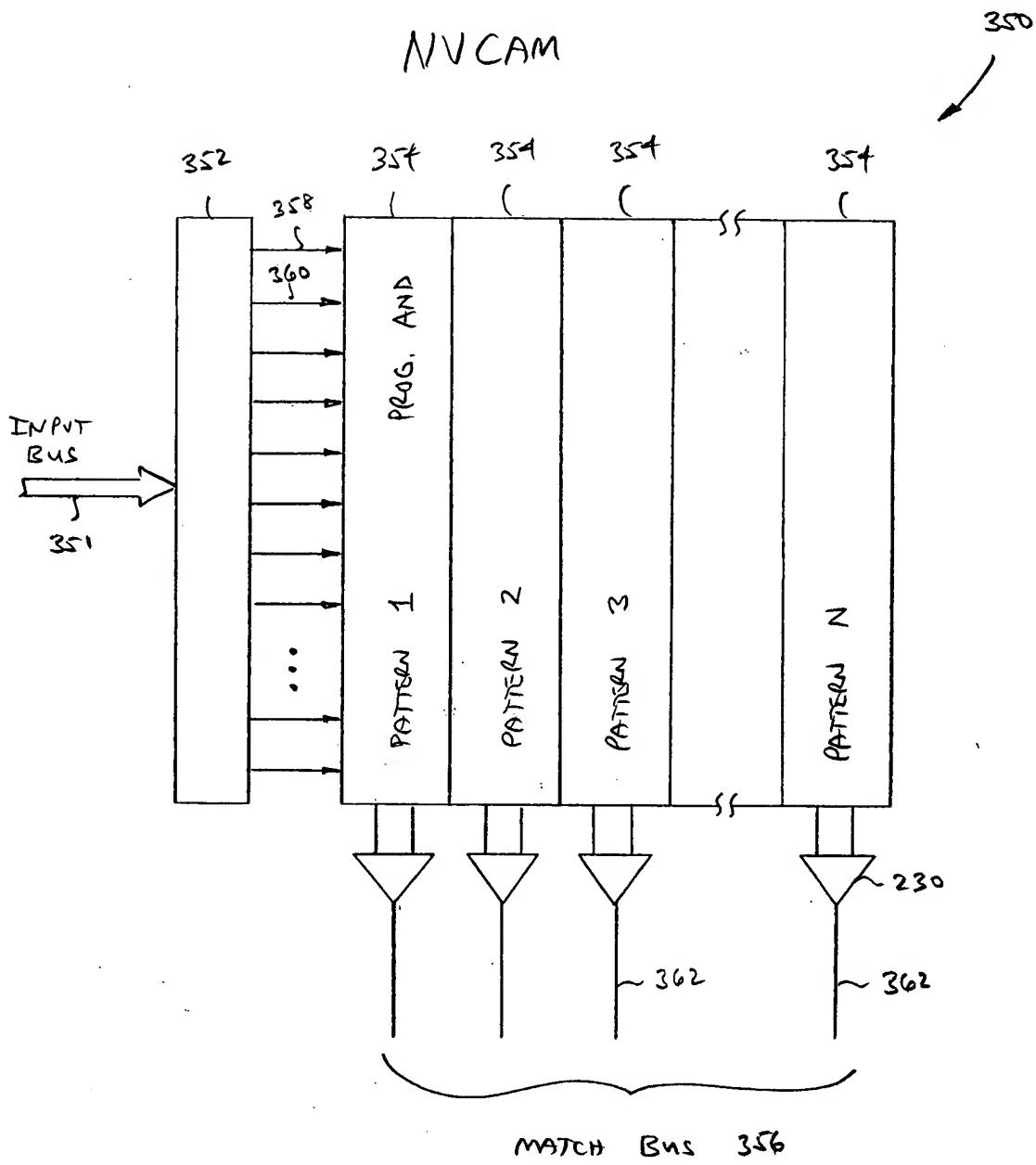


Fig. 10

